

ABSTRACT:

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The present invention relates to a device for comparison CMP, which is designed to emit a control signal Vcnt, which is representative of a difference which exists between the input signal frequencies Vdir and Vref.

The device according to the invention includes:

- a phase/frequency comparator PD, which supplies a regulation signal Tun, which is subjected to pulse width modulation according to the difference observed;
- a current source, which is designed to emit a charge current Ics, with a value which is controlled by the regulation signal Tun; and
- a capacitive element Cs, which is designed to generate the control signal Vcnt, under the effect of the charge current Ics.

By means of a regulation signal Tun, which has a frequency which is virtually constant, the invention makes it possible to impose high-frequency variations on the control signal Vcnt.

Application: Frequency synthesizers

Reference: Figure 1